## **Refine Search**

## Search Results -

Terms	Documents	
L1 and LDMOS	1	

US Patents Full-Text Database US OCR Full-Text Database

Database:

EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

US Pre-Grant Publication Full-Text Database

Search:

L7	\[\sigma\]	Refine Search
Recall Text	Clear	Interrupt

## **Search History**

DATE: Tuesday, November 08, 2005 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
DB=U	SPT; PLUR=YES; OP=OR		
<u>L7</u>	L1 and LDMOS	1	<u>L7</u>
<u>L6</u>	MOSFET and (("multilayered" near8 contact) same (via or feedthrough))	6	<u>L6</u>
<u>L5</u>	L4 and via	2	<u>L5</u>
<u>L4</u>	metalization and "multilayered contact"	5	<u>L4</u>
<u>L3</u>	L2 and mosfet	0	<u>L3</u>
<u>L2</u>	"multilayered contact"	19	<u>L2</u>
<u>L1</u>	6707102	1	<u>L1</u>

END OF SEARCH HISTORY